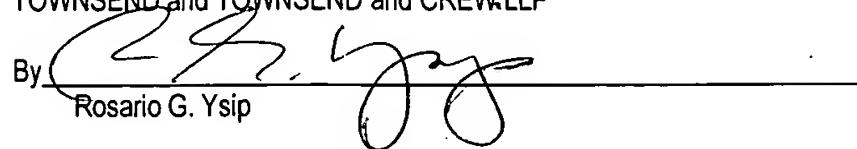


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TOWNSEND and TOWNSEND and CREW, LLP

By   
Rosario G. Ysip

PATENT

Attorney Docket No.: 015114-070200US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Thomas H. White, et al.

Application No.: 10/708,478

Filed: March 5, 2004

For: TECHNIQUES FOR  
CONFIGURING PROGRAMMABLE  
LOGIC USING ON-CHIP  
NONVOLATILE MEMORY

Examiner: CHANG, Daniel D.

Art Unit: 2819

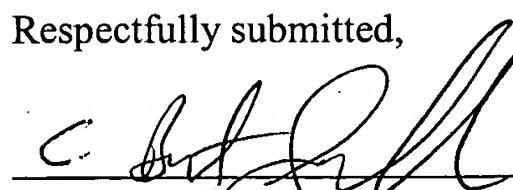
LETTER TO OFFICIAL DRAFTSPERSON

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Alexandria, VA 22313-1450

Sir:

Applicants submits six (6) sheets of formal drawings to be made of record in the above-identified case.

Respectfully submitted,

  
C. Bart Sullivan  
Reg. No. 41,516

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